



Clock Generator

Playback Designs' players and DACs employ technology never previously utilized in this industry. Our PDFAS (Playback Designs Frequency Arrival System) is a ground breaking technology developed by Andreas Koch, that eliminates the need for conventional PLLs, also known as "Phase Locked Loops". Conventional PLLs' ability to control and minimize jitter is limited by their inherent need to maintain phase coherence with the incoming signal. PDFAS is not limited by this requirement and therefore eliminates completely the effects of transmission induced clock jitter. Regardless of the source, whether it is an external transport, music server, personal computer, portable MP3 or disc player, the connection to the Playback Designs is effortless and jitter free.